

Homework 3
CS 654
Due: **Tuesday** March 25

1. Do problem 2.12 from the book. This problem uses the code and latencies from problem 2.1. You will be using instead the code and latencies of the first problem of Homework 2. For your convenience this data is included below.

		Latencies beyond single cycle

Loop:	LD F2, 0(Rx)	Memory LD +4
I0:	DIVD F8, F2, F0	Memory SD +1
I1:	MULTD F2, F6, F2	Integer ADD , SUB +0
I2:	LD F4, 0(Ry)	Branches +1
I3:	ADDD F4, F0, F4	ADDD +1
I4:	ADDD F10, F8, F2	MULTD +5
I5:	ADDI Rx, Rx, #8	DIVD +12
I6:	ADDI Ry, Ry, #8	
I7:	SD F4, 0(Ry)	
I8:	SUB R20, R4, Rx	
I9:	BNZ R20, Loop	